# Ref No: Ex/EE/T/322/2018(Old) B.E. ELECTRICAL ENGINEERING 3<sup>RD</sup> YEAR 2<sup>ND</sup> SEMESTER EXAMINATION, 2018 (OLD)

### DIGITAL SIGNAL PROCESSING

Full Marks 100

Time: Three hours

(50 marks for each part)

Use a separate Answer-Script for each part

No. of Questions	PART- I	Mar ks
	Answer any THREE questions Two marks reserved for neatness	· S
1. (a)	The impulse response of a causal discrete-time LTI system, is	
	$h_n = \{1,1,1\}$	8
	Find the output sequence $y_n$ , when the system is excited by a sequence	
	$x_n = \{1,-1,2,-1\}$	
( b)	Determine the inverse Z-transform of	
	$X(z) = \frac{5z^2 - 3z}{(z^2 - 4z + 3)}$ , for all possible ROCs of $X(z)$ .	8
2. (a)	Explain, with the help of relevant illustrations, how the left-half of the s-plane maps on to the z-plane.	8
(b)	The output $y_n$ and the input $x_n$ of a discrete-time linear time-invariant system are related by the following difference equation. $0.5y_n + 0.05y_{n-1} - 0.36y_{n-2} = 0.35 x_n + 0.126 x_{n-2}$	8

[Turn over

Ref No: Ex/EE/T/322/2018(Old)

30.00	Rei No. Extentibulo(C	-
No. of Questions	PART I	Marks
	Derive and draw the following structures for realizing the system.	
	(i) Direct Form- I (ii) Direct Form- II	
3. (a)	For discretisation of analog filters using backward difference transformation, show with the help of necessary mathematical derivation, how the $j\omega$ axis in the s-plane maps on to the z-plane.	6
(b)	Using bilinear transformation with frequency prewarping, design a digital filter corresponding to the analog filter with transfer function	10
	$H(s) = \frac{1}{(s+4)(s+1)}$	
	Consider a sampling frequency of 50 Hz.	
4. (a)	Consider the bandlimited analog signal	10
	$x(t) = 2\sin(200\pi t) + 3\sin(600\pi t)$ , where t is in seconds.	
	Find the Nyquist sampling rate for the above signal. If the above signal is sampled 125 times per second, and then the	
	resulting sampled signal $\mathcal{X}_n$ is converted back to an analog signal using an ideal lowpass filter with a cutoff frequency of 62.5 Hz, obtain the values of the frequencies in the reconstructed analog signal.	
(b)	Determine the Z-transforms and their ROCs for the following sequences. $x[n] = ASin(\omega_0 n\tau)u[n]$	3+3
	$g[n] = n^2 e^{-an\tau} u[n]$	

## Ref No:Ex/EE/T/322/2018(Old)

	PART- I	
5.	Write short notes on any two of the following.  (a) Designing digital filters by impulse-invariant transformation.  (b) Modeling uniform sampling by 'Pulse Amplitude Modulation'.  (c) Recursive and non-recursive filters.  (d) Causality and stability of DTLTI systems.	
2		

[Turn over

Ref No: Ex/EE/T/322/2018 (Old)

# B.E. ELECTRICAL ENGINEERING THIRD YEAR SECOND SEMESTER EXAM 2018 (Old)

#### SUBJECT: - DIGITAL SIGNAL PROCESSING

Time: Three hours

Full Marks 100 (50 marks for each part)

Use a separate Answer-Script for each part No. of PART II Marks Questions Answer any three questions. TWO marks are reserved for neat and well organized answers. 1. (a) Describe in detail how can an (N/2)-point DFT be divided into two even and 10 odd harmonic (N/4)-point DFTs? (b) Prove that, for FIR filters in offline operations, the length of the output 06 sequence is always smaller than the length of the input sequence. 2. (a) How can you compute 4-point FFT of a discrete sequence using Radix-2 10 decimation-in-frequency in-place FFT algorithm? Draw the corresponding signal flow graph. (b) Describe the non-causal and causal forms of Bartlett, Hamming and 06 Blackman windows employed for designing FIR filters. Hence comment on the approximate widths of main lobes of their frequency responses. 3. (a) A 7-tap causal linear-phase FIR brick-wall type low-pass filter has been 10 designed with unity pass band gain and a cut off frequency of 200 Hz. The sampling frequency has been chosen as 1000 Hz. The design employed Raised Cosine window for smoothing filter coefficients. Determine the filter coefficients. Also draw the schematic realization of the filter. "Both FIR low pass and high pass image filters should comprise only 06 positive coefficients." - Justify or correct this statement, citing suitable reasons. 4. (a) Give a detail account of the processor architecture of TMS320C25. How is 08 multiply/accumulate operation carried out in this processor? Differentiate between group delay and phase delay of a distortion-less filter. 05+03What is Gibbs phenomenon? Write short notes on any two of the following: 08×2 (i) Inverse discrete Fourier transform. =16(ii) Periodicity and symmetry properties of linear phase digital filters. Histogram equalization for contrast enhancement in images.