B. ELECTRICAL ENGINEERING 2ND YEAR 1ST SEMESTER EXAMINATION, 2023

Subject: CIRCUIT THEORY Time: Three Hours Full Marks: 100

<u>Part I</u> (50 marks) Use Separate Answer-script for Each Part

Question Answer Any Three questions (3×16)
No. (Two marks are reserved for well organized answers)

Marks

Q1 (a) Given the circuit of Figure Q1(a), use nodal analysis to solve for the voltage Vab.

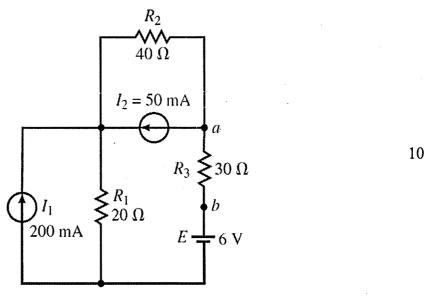


Figure Q1(a)

Q1 (b) Find the Norton equivalent for the circuit shown in Figure Q1(b) with respect to the terminals a and b. Determine the current through R_L.

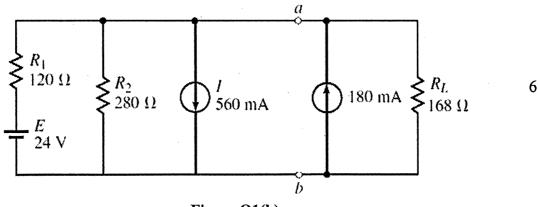
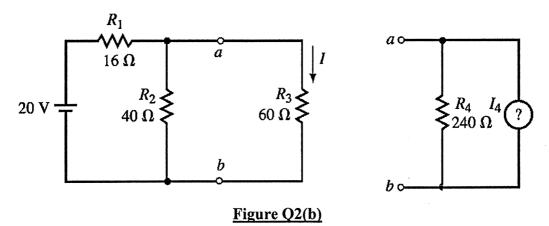


Figure Q1(b)

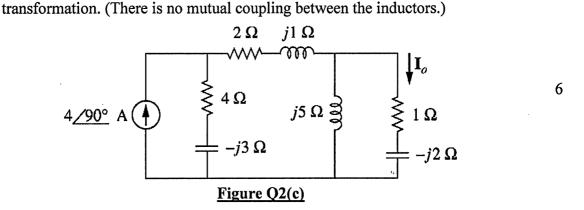
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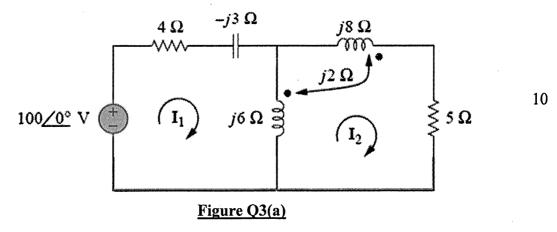
- Q2 (a) With the help of an example, briefly explain the Compensation Theorem.
 - (b) If the indicated portion in the circuit of Figure Q2(b) is to be replaced with a current source and a 240-ohm shunt resistor, determine the magnitude and direction of the required current source.



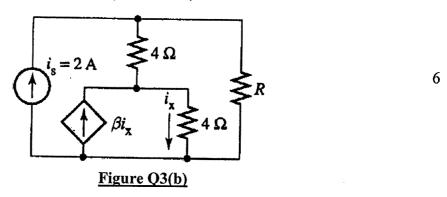
(c) Find I₀ in the circuit, as given in Figure Q2(c), by using the concept of source



Q3 (a) For the circuit shown in Figure Q3(a) determine the currents I_1 and I_2 .



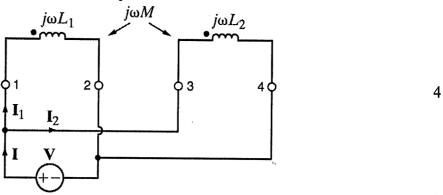
Q3 (b) For the circuit, as shown in Figure Q3(b), find the value of R that results in maximum power absorbed by R for $\beta = 0.5$ and $\beta = 1.5$.



Q4 (a) With the help of neat diagrams show how the reference polarity of the mutually induced voltage depends on the direction of the inducing current and the dots on the two coupled coils.

Figure Q4(b)

(b) Two mutually coupled inductors are connected across a voltage source, as shown in Figure Q4(b). Obtain the equivalent inductance of the circuit.



(c) For the network shown in Figure Q4(c), write a Tie-Set schedule and then obtain the loop equilibrium equations using the Graph

Theoretic approach.

50V \star S0V \star S0V S0

Figure Q4(c)

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Q5 (a) Justify the following statement:

"All Paths are Subgraph but all Subgraphs are not Path".

Q5 (b) Consider the network shown in Figure Q5(b). For a tree consisting of (1, 2, 3) obtain the Cut-Set matrix.

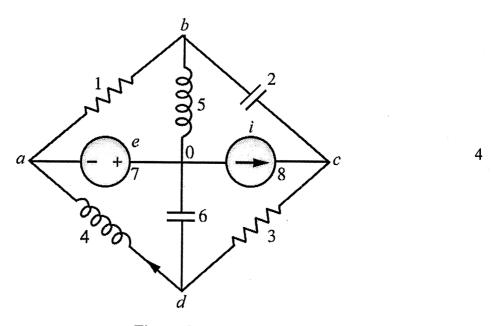
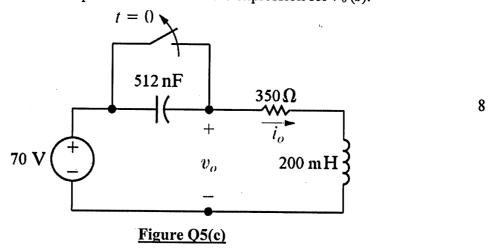


Figure Q5(b)

(c) For the circuit shown in Figure Q5(c) the switch is opened at t=0. Transform the circuit to its s-domain equivalent and obtain the expression for $V_0(s)$.



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B.E. ELECTRICAL ENGINEERING SECOND YEAR FIRST SEMESTER EXAM 2023

SUBJECT: - CIRCUIT THEORY

Full Marks: 100 (50 marks for this part)

Time: Three hours

Use a separate Answer-Script for each part		
No. of	PART -II	Marks
Question	Answer any Three	
1)	(Two marks reserved for well organized answers) Consider the circuit shown in Fig.P-1. The switch is thrown from position 1 to position 2 at time $t=0$. Just before the switch is thrown,	(12+4 =16)
	the initial conditions are $i_L(0^+) = 2A, v_C(0^+) = 2V$. Find the current	•
	$i(t)$ after the switch thrown at position 2. Assume $L=1H, R=3\Omega, C=0.5F, V_1=5V$.	
	Sketch the waveform of $i(t)$.	
	V(t) + + C + C	
	Fig. P-1.	
	rig. r-1.	
2)	Find the Laplace transform of the periodic signal $x(t)$ as shown in Fig. P-2. Derive the formula that you have used.	(8+8 =16)
	0 1 2 3 4 5 t (sec)	
	Fig. P-2.	
3) (a)	The port 1 of the two port network shown in Fig. P-3 (a) is connected to a 200 mV DC source in series with a 1000 Ω resistance. The port-2 of the network is loaded with a resistance of 2.5k Ω . Determine the Z-parameter of the network and the power fed to the load.	(12)

B.E. ELECTRICAL ENGINEERING SECOND YEAR FIRST SEMESTER EXAM 2023 SUBJECT: - CIRCUIT THEORY

Full Marks: 100 (50 marks for this part)

