Digital Controllers

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In a processor based digital controller, rapid switching from one algorithm to another (e.g. a P controller to a PID controller) and automatic tuning of controller parameters are possible.







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A large ' τ ' may lead to **unstable operation** of the loop (because of the extra lag introduced in the loop), whereas a very small ' τ ' requires a **high speed digital hardware** (hence high cost) to implement the controller.

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where, $K_p = proportional gain$, $b_n = fixed bias$.

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Realization of the P controller:



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$$= \int_{0}^{n\tau-\tau} e.dt + \int_{n\tau-\tau}^{n\tau} e.dt \quad \text{for n = 1,2,3,..}$$

Now, $\int_{o}^{n\tau-\tau} e.dt = \int_{o}^{(n-1)\tau} e.dt = I_{n-1}$

Then, $I_n = I_{n-1} + \int_{(n-1)\tau}^{n\tau} e.dt$

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The second term of the above relation represents the area under the curve 'e' for $(n - 1)\tau \le t \le n\tau$.

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The second term of the above relation represents the area under the curve 'e' for $(n - 1)\tau \le t \le n\tau$.

This area may be approximated by the shaded rectangle (called the **method of rectangular integration**) as







Using the notation $\mathbf{e}_{\mathbf{n}}$ for $\left. \mathcal{e} \right|_{t=n\tau}$, $I_n = I_{n-1} + \tau e_n$

Now, output m' at the nth instant may be expressed as

$$m'_n = K_p \left[e_n + \frac{I_n}{T_i} \right]$$
 where $I_n = I_{n-1} + \tau e_n$

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$$m'_{n-1} = K_p \left[e_{n-1} + \frac{I_{n-1}}{T_i} \right]$$

The difference between these two outputs is

$$m'_{n} - m'_{n-1} = K_{p} \left[e_{n} - e_{n-1} + \frac{1}{T_{i}} (I_{n} - I_{n-1}) \right]$$

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or,
$$m'_{n} = e_{n} K_{p} \left(1 + \frac{\tau}{T_{i}} \right) - K_{p} e_{n-1} + m'_{n-1}$$

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$$\begin{split} m'_{n} - m'_{n-1} &= K_{p} \left[e_{n} - e_{n-1} + \frac{\tau e_{n}}{T_{i}} \right] \\ \text{or,} \quad m'_{n} &= e_{n} K_{p} \left(1 + \frac{\tau}{T_{i}} \right) - K_{p} e_{n-1} + m'_{n-1} \\ &= a_{o} e_{n} + a_{1} e_{n-1} + m'_{n-1}, \quad \text{say} \\ \text{where,} \quad a_{o} &= K_{p} \left[1 + \frac{\tau}{T_{i}} \right] \text{ and } \quad a_{1} = -K_{p} \end{split}$$

Realization of the PI Controller

Controller output at the nth instant:

 $m_n = m'_n + b_n$ $m'_n = a_0 e_n + a_1 e_{n-1} + m'_{n-1}$



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 $m'_{n} = a_{0}e_{n} + a_{1}e_{n-1} + m'_{n-1}$ e_{n-1} a₁ b_n (bias) delay m'n ► m_n **e**_n a_o Controller error delay τ output sequence **m'**_{n-1} sequence

Problem: Develop a digital PI Controller using Trapezoidal rule for integration.
Velocity or incremental form of PI controller

The controller output is proportional to the derivative of a standard PI controller and it may be expressed as (without bias):

$$\Delta m_n = m_n - m_{n-1} = m'_n - m'_{n-1} = a_o e_n + a_1 e_{n-1}$$

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Velocity form of controller is useful when the actuator is some kind of adder (integral action), like a stepping motor.

Software Realization of the PI Controller



```
# include < studio.h>
void main (void)
    {
    float e = 0, e1, m, mp = 0;
    float a0, a1, b;
    float adc (void) ; // digitized error
    void dac (float m ) ; // analog output
    a0 = ------; // Kp [1 + τ/Ti]
    a1 = -----; // -Kp
    b = -----; // bias
```

Software Realization of the PI Controller



```
for (;;) // continuous loop

{ // loop time is the sampling interval \tau

e1 = e;

e = adc ();

mp = mp + a0*e + a1*e1;

m = mp + b;

// provision for saturation

if (m < 0) m = 0;

if (m > 100) m = 100;

dac (m);

}
```

Software Realization of the PI Controller



```
float adc (void) // Analog-to-digital conversion
    {
      float v;
      scanf ("%f", &v); // for (keyboard) simulation
      return v; // (to be replaced for actual
      } // realization)
void dac (float m) // Digital-to-analog conversion
      {
      printf ("%f\n", m); // for (VDU) simulation
      } // (to be replaced for actual realization)
```

Proportional-Derivative (PD) Controller

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D_n may be approximated using the **backward difference algorithm** as

$$D_n \approx \frac{e_n - e_{n-1}}{\tau}$$

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$$m_{n} = K_{p} \left[e_{n} + T_{d} \left(\frac{e_{n} - e_{n-1}}{\tau} \right) \right] + b_{n}$$

or
$$m_n = K_p \left[1 + \frac{T_d}{\tau} \right] e_n - \left[\frac{K_p T_d}{\tau} \right] e_{n-1} + b_n$$

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$$= a_o e_n + a_1 e_{n-1} + b_n$$
 where $a_o = K_p \left(1 + \frac{T_d}{\tau}\right)$ and $a_1 = -\frac{K_p T_d}{\tau}$

Realization of the PD Controller



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Problem: Develop a 'c' program for software realization of the PD Controller.

Provision for anti-derivative kick

To avoid derivative action from a sudden change in set-point, the derivative action is generally derived from the measured output.

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Thus, the controller output may be expressed as,

$$m = K_p \left(e - T_d \frac{dc}{dt} \right) + b$$

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using backward difference algorithm, the controller output at the nth instant is

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$$\begin{split} m_n &= K_p \left[e_n - T_d \left(\frac{c_n - c_{n-1}}{\tau} \right) \right] + b_n \\ \text{or } m_n &= K_p e_n - \frac{K_p T_d}{\tau} c_n + \frac{K_p T_d}{\tau} c_{n-1} + b_n \\ &= a_o e_n + p_o c_n + p_1 c_{n-1} + b_n \quad \text{where} \\ p_o &= -\frac{K_p T_d}{\tau} \\ p_1 &= \frac{K_p T_d}{\tau} = -p_o \end{split}$$

Realization of PD Controller with anti-derivative kick



Realization of PD Controller with anti-derivative kick



Problem: Develop a 'c' program for software realization of the PD Controller with anti-derivative kick

Proportional-Integral-Derivative (PID) Controller

The analog controller output is

$$m = K_{p} \left[e + \frac{1}{T_{i}} \int_{o}^{t} e dt + T_{d} \frac{de}{dt} \right] + b$$
$$= m' + b \quad (say)$$
where $m' = K_{p} \left[e + \frac{1}{T_{i}} \int_{o}^{t} e dt + T_{d} \frac{de}{dt} \right]$

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where $m' = K_{p} \left[e + \frac{1}{T_{i}} \int_{o}^{t} e dt + T_{d} \frac{de}{dt} \right]$

The controller output (without bias) at the nth instant, using backward difference algorithm, is

$$m'_{n} = K_{p} \left[e_{n} + \frac{I_{n}}{T_{i}} + T_{d} \left(\frac{e_{n} - e_{n-1}}{\tau} \right) \right]$$

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The controller output at the (n-1)th instant is

$$m'_{n-1} = K_p \left[e_{n-1} + \frac{I_{n-1}}{T_i} + T_d \left(\frac{e_{n-1} - e_{n-2}}{\tau} \right) \right]$$

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$$m'_{n-1} = K_p \left[e_{n-1} + \frac{I_{n-1}}{T_i} + T_d \left(\frac{e_{n-1} - e_{n-2}}{\tau} \right) \right]$$

Subtracting,

$$m'_{n} - m'_{n-1} = K_{p} \left[e_{n} - e_{n-1} + \frac{I_{n} - I_{n-1}}{T_{i}} + \frac{T_{d}}{\tau} \left(e_{n} + e_{n-2} - 2e_{n-1} \right) \right]$$

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Using rectangular integration algorithm,

 $I_n - I_{n-1} = \tau e_n$

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Using rectangular integration algorithm,

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then,
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or,
$$m'_{n} = e_{n} \left(1 + \frac{\tau}{T_{i}} + \frac{T_{d}}{\tau} \right) K_{p} - e_{n-1} \left(\frac{2T_{d}}{\tau} + 1 \right) K_{p} + e_{n-2} \left(\frac{K_{p}T_{d}}{\tau} \right) + m'_{n-1}$$

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or,
$$m'_n = a_o e_n + a_1 e_{n-1} + a_2 e_{n-2} + m'_{n-1}$$

$$m'_{n} = a_{o}e_{n} + a_{1}e_{n-1} + a_{2}e_{n-2} + m'_{n-1}$$

where,
$$a_{o} = K_{p} \left(1 + rac{ au}{T_{i}} + rac{T_{d}}{ au}
ight)$$

$$a_1 = -K_p \left(\frac{2T_d}{\tau} + 1\right)$$

and

$$a_2 = \frac{K_p T_d}{\tau}$$

Realization of the PID Controller

$$m'_{n} = a_{o}e_{n} + a_{1}e_{n-1} + a_{2}e_{n-2} + m'_{n-1}$$

and $m_n = m'_n + b_n$



Problems:

- 1. Develop a digital PID controller using trapezoidal rule for integration
- 2. Develop a program in 'C' for software realization of the PID controller
- 3. Modify the above controller to provide anti-derivative kick feature

Techniques used for anti-integral windup
> By saturating or limiting the integral value

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> By resetting the integral value to zero

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- > By saturating or limiting the integral value
- > By resetting the integral value to zero
- > By omitting the integral term
- > By adaptive adjustment of controller parameters

Some anti-integral windup schemes

Stop integration when PI/PID controller internal output (prior to the saturation block) exceeds the saturation limits



Scheme for PI Controller

Performance of anti-integral windup scheme



Scheme for PI Controller



PI control without anti-integral windup



PI control with anti-integral windup

Some anti-integral windup schemes

Reduce integration gradually as PI/PID controller internal output exceeds the saturation limits



G : a constant

Some anti-integral windup schemes

Clegg integrator – the integrator is set to zero (reset) when the error crosses zero



- > Automatic mode means automatic closed loop operation
- > Manual mode means open loop manual control





If there is any difference between the controller output and the manual command, a *bump* occurs in the process output when the switch position is altered.



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To provide 'bump-less transfer' from auto-to-manual change over, special arrangements may be made for 'set-point initialization'.



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To provide 'bump-less transfer' from auto-to-manual change over, special arrangements may be made for 'set-point initialization'.

The manual command is driven to equal the controller output when the loop is in AUTO mode.



When the loop is in MANual mode, if there is a steady error existing due to any difference between the set-point of the controller and the process output (under manual control), integral term, in case of PI and PID controllers, may wind-up to a large value, and consequently anti-integral wind-up is necessary for such situations.



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To provide *bump-less transfer* for all the operating modes, *incremental or velocity* from of controller is used with an additional integrator.

Scheme for bump-less transfer



Scheme for bump-less transfer



The incremental controller output (without bias) at the nth instant may be expressed as

$$\begin{split} \Delta m'_n &= m'_n - m'_{n-1} \\ &= e_n K_p \left(1 + \frac{\tau}{T_i} + \frac{T_d}{\tau} \right) - e_{n-1} K_p \left(\frac{2T_d}{\tau} + 1 \right) + e_{n-2} \frac{K_p T_d}{\tau} \quad \text{for a PID controller} \end{split}$$

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Scheme for bump-less transfer



The integrator may be represented as

$$\Delta m'_n \longrightarrow$$
 Integrator $\longrightarrow m'_n$

Scheme for bump-less transfer



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The integrator output may be represented as

$$m'_{n} = m'_{n} - m'_{n-1} + m'_{n-1}$$

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Scheme for bump-less transfer



The presence of integrator at the output ensures a smooth output variation even when the actual manual command is different from the actual controller output under closed-loop control.

Realization of the incremental type PID Controller



Suitable for processes with non-zero dead-time.

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Z-N settings							
Controller	Kp	Ti	T_d				
P – Controller	0.5 K _c						
PI – Controller	0.45 K _c	T _c /1.2					
PID – Controller	0.6 K _c	T _c /2	T _c /8				

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	Z-N settings			
Controller	Kp	Ti	T _d	
P – Controller	0.5 K _c			🖕 gain margin: 2
PI – Controller	0.45 K _c	T _c /1.2		
PID – Controller	0.6 K _c	T _c /2	T _c /8	

The critical gain K_c and critical time period T_c are determined from an experiment with relay (switching element) feedback.

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 \checkmark The process is driven by a square wave of amplitude 'D'.



Assuming the process to be a low-pass system, the process output 'c' contains mainly the fundamental component.


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Thus the error signal 'e' becomes sinusoidal,

 $e = A \sin \omega t$



The relay output 'v' may be found out as follows:







The Fourier series of the relay output (v) may be expressed as:

$$v = \frac{4D}{\pi} \left(\sin \omega t + \frac{1}{3} \sin 3\omega t + \frac{1}{5} \sin 5\omega t + \dots \right)$$



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The process practically attenuates all higher harmonics other than the fundamental.

Then the process gain at frequency 'ω' becomes

$$|G(\omega)| = \frac{output \ amplitude}{input \ amplitude} = \frac{A}{\frac{4D}{\pi}} = \frac{\pi A}{4D}$$





Now, to maintain steady oscillations at $\omega = \omega_c$, the loop gain is $1 \angle \pi$ (considering negative feedback).



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Also $\angle G(\omega_c) = \pi$, as relay phase shift is zero.



Thus by knowing the relay amplitude 'D' and by measuring the amplitude 'A' of the process output 'c', critical gain K_c may be determined $\begin{bmatrix} K_c = \frac{4D}{\pi A} \end{bmatrix}$.



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 T_{c} may be estimated by measuring the frequency of the output oscillation $T_{c} = \frac{2\pi}{\omega_{c}}$.

Block diagram of the Relay autotuner

(The Satt Control Autotuner by Satt Control, Sweden)



- $M \rightarrow Manual position$
- $A \rightarrow Auto position$
- $T \rightarrow$ Tune position

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The tuning is then activated by pushing the mode switch to tune position.





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> The modified relay amplitude is stored for the next tuning operation.

Satt Control Autotuner



 $M \rightarrow$ Manual position $A \rightarrow$ Auto position $T \rightarrow$ Tune position

The system is automatically switched to Auto mode after estimating the critical gain K_c and critical time period T_c during first 5½ period of oscillation.

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The system is automatically switched to Auto mode after estimating the critical gain K_c and critical time period T_c during first 5½ period of oscillation.

The parameters of PID controller (viz. K_p , T_i and T_d) are determined from K_c and T_c according to Z-N rule.

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